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on of information unless it contains a valid OAUS control number.

Sheet 1 of 1

Substitute from 1449APTO
INFORMATION DISCLOSURE
TRADE LEATEMENT BY APPLICANT Complete if Known 09/834,751 **Application Number** April 13, 2001 Filing Date (Use as many sheets as necessary) Velichko, Sergey **First Named Inventor Group Art Unit** 2857 Miller, Craig **Examiner Name** Attorney Docket No: 303.750US1

			US P	ATENT DOCUMENTS	
Exami: initial		USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
ml	b	US- 2005/0021273 A1	01/27/2005	Dorough, M. J., et al.	08/24/2004
m	व	US-6,549,863	04/15/2003	Morinaga, K.	08/21/2000
m	6	US-6,563,331	05/13/2003	Maeng, J. S.	08/21/2001
M		US-6,859,760	02/22/2005	Dorough, M. J.	05/20/2003

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T²				

	O.T. 1.E	DOOLINGSTO NON DATENT LITEDATUDE DOCUMENTS	
	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No '	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	7
		CHEUNG, D., et al., "The INMOS Integrated Paramatric Test and Analysis	}
المالية		System", Proceedings IEEE 1989 International Conference on Microelectronic	
MID		Test Structures, vol. 2(1), (Mar., 13-14, 1989),45-50	<u> </u>
		GRANIERI, M. N., "System Engineering Considerations and Methodology for	
mlb		Effecting a Cohesive Functional/Parametric Testing Strategy", IEEE Automatic	
		Testing Conference, (Sep., 25-28, 1989),1-8	
		HAMILTON, A., et al., "An Expert System for Process Diagnosis [MOS Product	
mb		Testing]", Proceedings IEEE 1989 International Conference on Microelectronic	
		Test Structures, vol. 2(1), (Mar., 13-14, 1989),55-57	
110		MADGE, R., et al., "In Search of the Optimum Test Set - Adaptive Test Methods	
MIP		For Maximum Defect Coverage and Lowest Test Cost", IEEE International Test	Ì
·		Conference, (Oct., 26-28, 2004),	
		PEREZ, R. A., et al., "Integrating Expert Systems With A Relational Database In	
mlb		Semiconductor Manufacturing", IEEE Transactions on Semiconductor	}
•		Manufacturing, vol. 6(3), (Aug., 03, 1993),199-206	
1,		WEBER, C., "Standardization of CMOS Unit Process Development",	
mly		Proceedings IEEE 1989 International Conference on Microelectronic Test	
		Structures, (Mar., 13-14, 1989),39-44	<b>I</b>

EXAMINER Manuel 1 Bulean DATE CONSIDERED 1/19/06			_
	EXAMINER	DATE CONSIDERED 1/19/	06